- 1. Recalling the compiler toolchain diagram in the text: what are the input and output of the *compiler*?
- The MIPS architecture supports a number of registers that are specifically designed to support functions: \$ra, \$sp, \$fp, \$v0/\$v1, \$a0-\$a3, etc.

- 1. Recalling the compiler toolchain diagram in the text: what are the input and output of the *assembler*?
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- 1. Recalling the compiler toolchain diagram in the text: what are the input and output of the *linker*?
- The MIPS architecture supports a number of registers that are specifically designed to support functions: \$ra, \$sp, \$fp, \$v0/\$v1, \$a0-\$a3, etc.

- 1. Recalling the compiler toolchain diagram in the text: only the linker takes more than one input. What is the linker's role?
- The MIPS architecture supports a number of registers that are specifically designed to support functions: \$ra, \$sp, \$fp, \$v0/\$v1, \$a0-\$a3, etc.